

ABSTRACT:

To provide a circuit arrangement, particularly for a television, multimedia, radio or video recording device, for transition from a range of low voltage (U_i) to a range of higher voltage (U_o) having a simple and low-cost structure and operating as an interface between the range of low voltage (U_i) and a range of higher voltage (U_o) in a stable and

5 reliable way, also when the voltage difference between the range of low voltage (U_i) and the range of higher voltage (U_o) is large, it is proposed that at least one adapter circuit (10) is provided, which amplifies a particularly analog input signal of a low current (I_i) by an amplification factor (n) into a particularly analog output signal of a higher current (I_o),

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- whose input (12) is assignable to the range of low voltage (U_i),
- whose output (18) is assignable to the range of higher voltage (U_o), and
- comprises at least one npn transistor current mirror (14) and
- at least one pnp current mirror (16) arranged in series with the npn transistor current mirror (14) and connected to at least one high voltage source (30).

15 Fig. 1